

Notice of References Cited	Application/Control No. 10/764,606	Applicant(s)/Patent Under Reexamination SHIMIZU ET AL.	
	Examiner Eric B. Chen	Art Unit 1765	Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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*	B	US-5,876,267	03-1999	Kanda, Shinji	451/38
*	C	US-2003/0007772	01-2003	Borrelli et al.	385/137
*	D	US-4,012,263	03-1977	Shell, James Allen	501/70
*	E	US-6,407,866	06-2002	Yamashita et al.	359/619
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf et al., Silicon Processing for the VLSI Era, 1986, Lattice Press, Vol. 1, pp. 532-33.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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